

# Active and Passive Testing in an Industrial Setting

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# Motivation

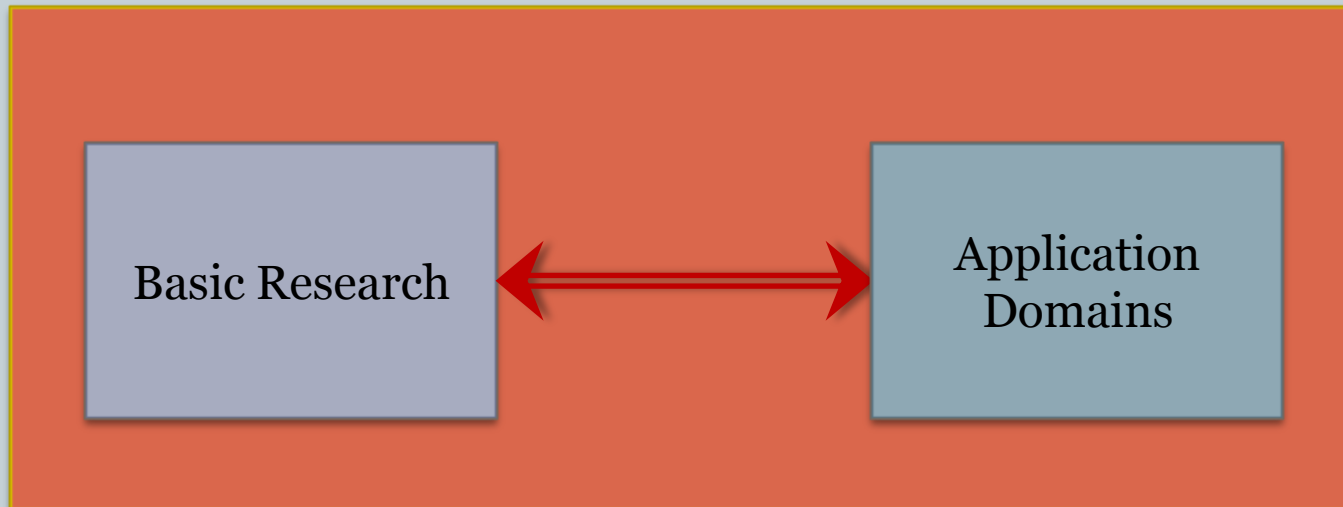
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- Show the evolution of active testing to monitoring (passive testing) techniques
- Explain the differences and complementarity of these techniques
- Application to an industrial case study

# A collaborative model of research

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- Our research model is based in:
  - Basic and applied research
  - Evaluation of results in real environments
  - Strong collaboration with industrial partners



# Different Application Domains

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DETAILED WEB SERVICES PROCESS

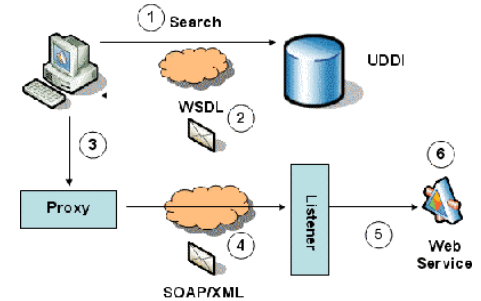
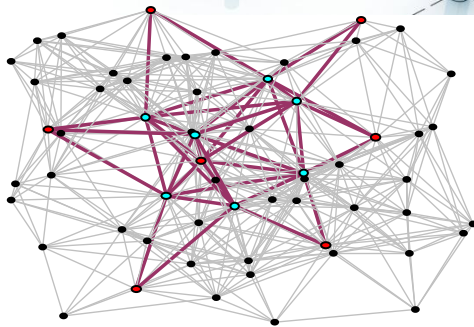


Figure 1: The process flow of a Web service



Applications		
JINI	WAP	
SDP	TCP/IP	RFCOMM
L2CAP		
Link Manager		
ACL	SCO	
Baseband		
Bluetooth Radio		



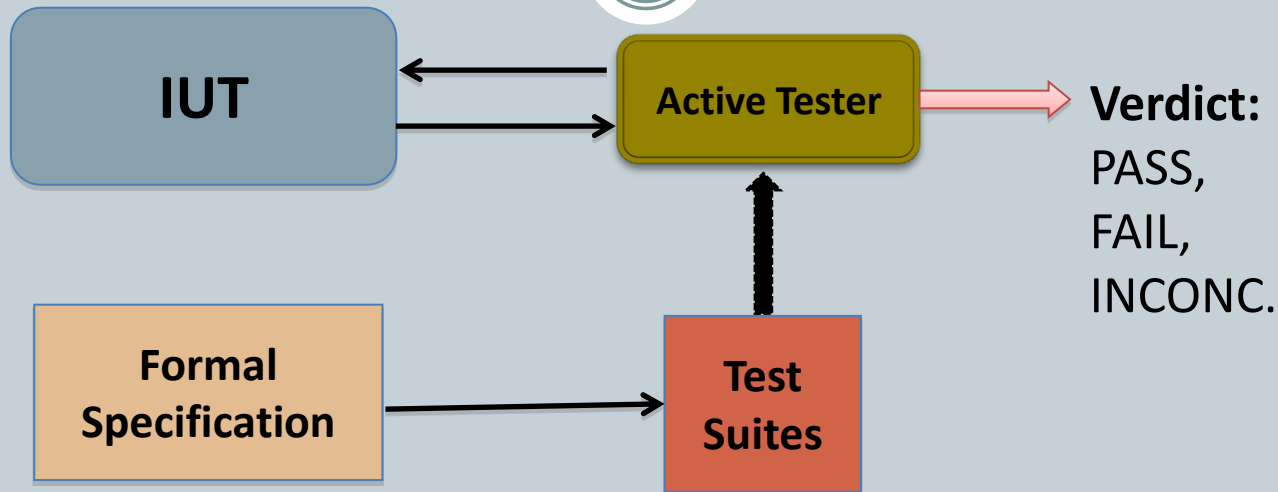
# Testing

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- **Testing:** The process of executing software with the intent of finding and correcting faults
- **Conformance testing:** The process of checking if the implementation under test conforms the specification
  - Two techniques: active and passive testing (monitoring)
  - This presentation will focus on both of them, to show that there are many common objectives and challenges

# What is active testing ?

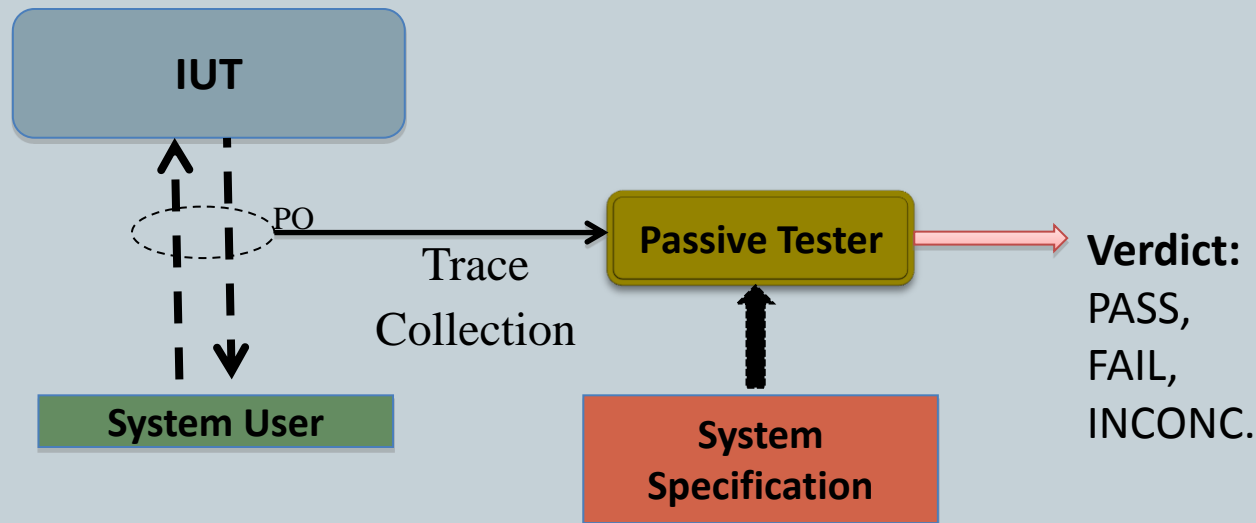
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- Usually called Model Based Testing (MBT)
- It is assumed that the tester controls the implementation. Control means: after sending an input and after receiving an output, the tester knows what is the next input to be send
- The tester can guide the implementation towards specific states
- Automatic test generation methods can be defined
- Usually a test case is a set of input sequences

# What is monitoring (passive testing) ?

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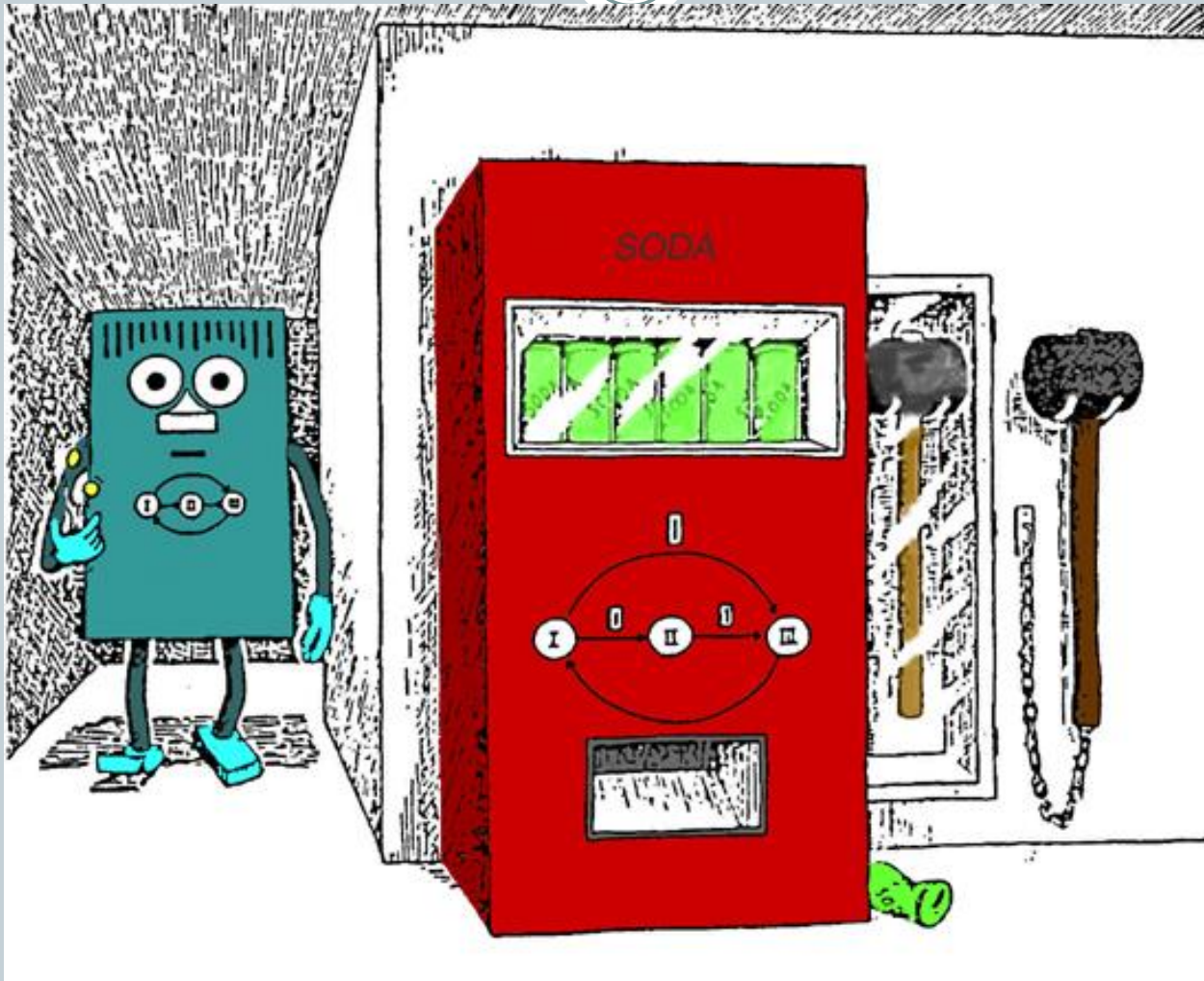


- Passive testing consists in analyzing the traces recorded from the IUT and trying to find a fault by comparing these traces with either the complete specification or by verifying some specific requirements (or properties) during normal runtime
- No interferences with the IUT
- It is also referred to as monitoring

# Fault models for active testing

## Example: Soda vending machine

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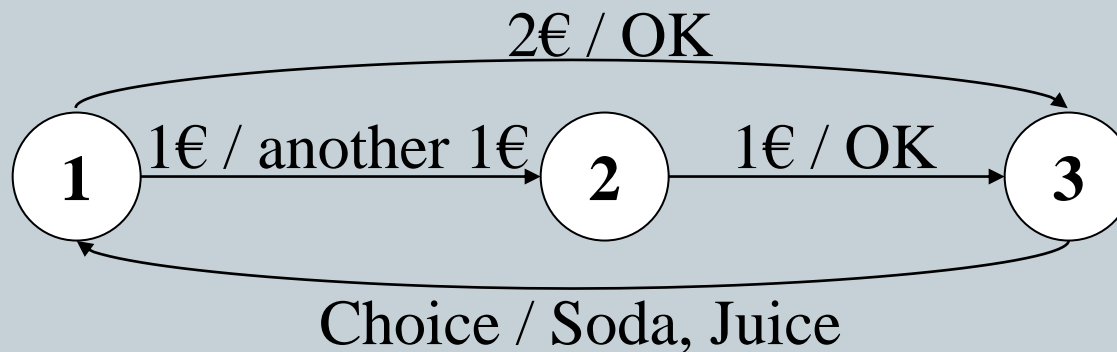




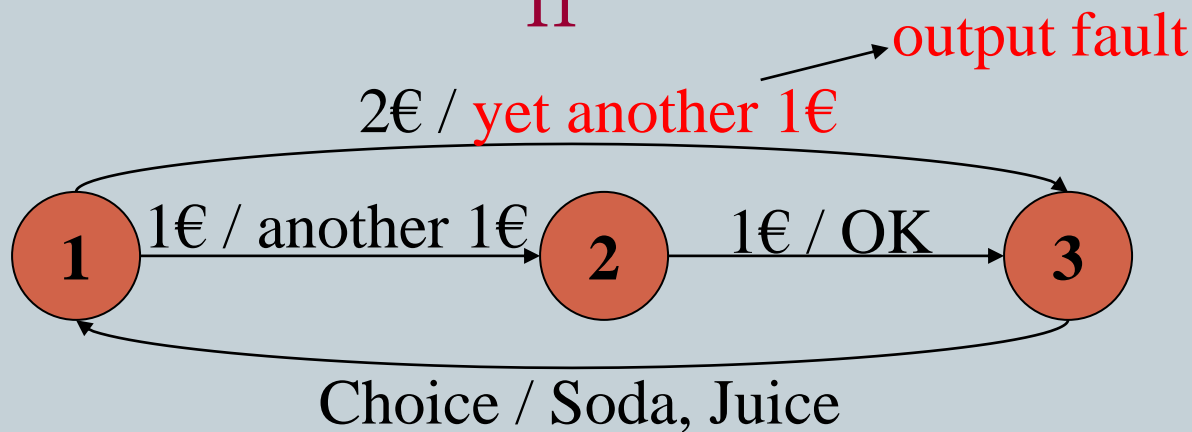
# Example: Soda Vending Machine

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## Specification



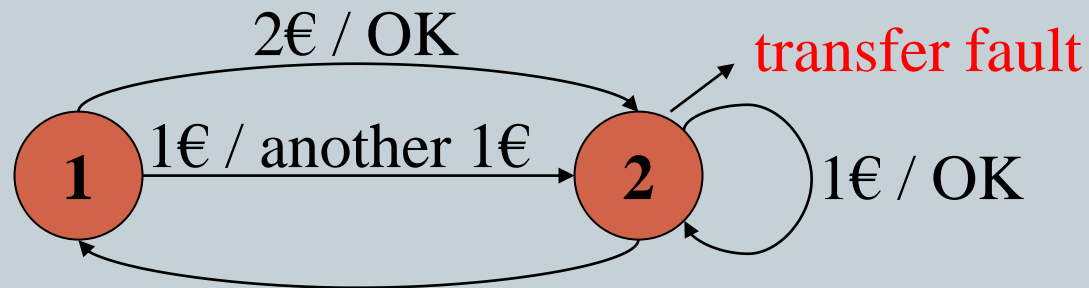
I<sub>1</sub>



# Example: Soda Vending Machine

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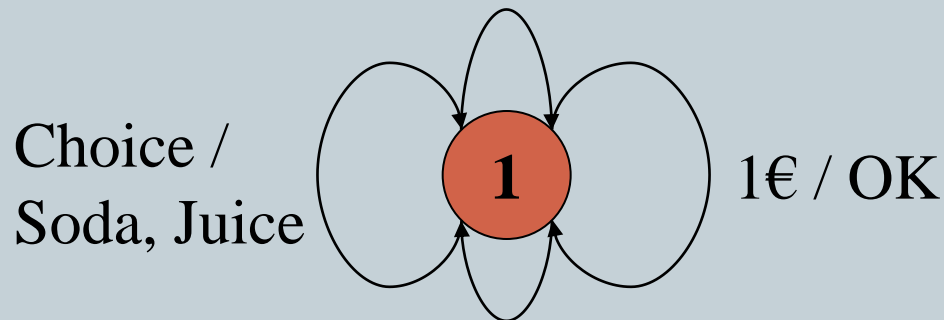
**I<sub>2</sub>**



Choice / Soda, Juice

**I<sub>3</sub>**

1€ / another 1€



2€ / OK

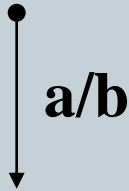
# Controllability issue in active testing

- How to bring the finite state machine implementation into any given state at any given time during testing ?
  - Non trivial problem because of limited controllability of the finite state machine implementation
  - It may not be possible to put the finite state machine into the head state of the transition being tested without realizing several transitions

# Controllability: examples

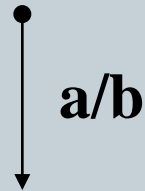
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**Specification**



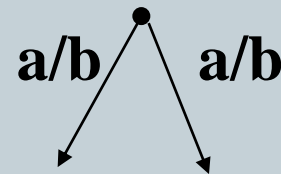
**Controllable**

**Imp1**



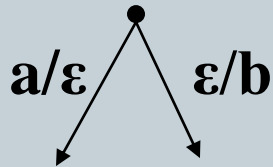
**Non controllable**

**Imp2**



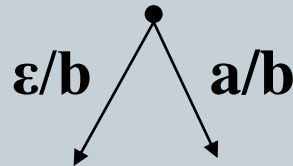
**Non controllable**

**Imp3**

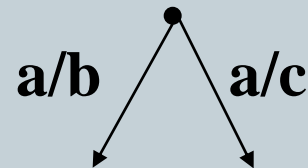


**Controllable under fairness assumption**

**Imp4**



**Imp5**



# Observability issue in testing

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- How to verify that the finite state machine implementation is in a correct state after input/output exchange?
  - *State identification* problem. Difficult because of limited observability of the finite state machine implementation, it may not be possible to directly verify that the finite state machine is in the desired tail state after the transition has been fired

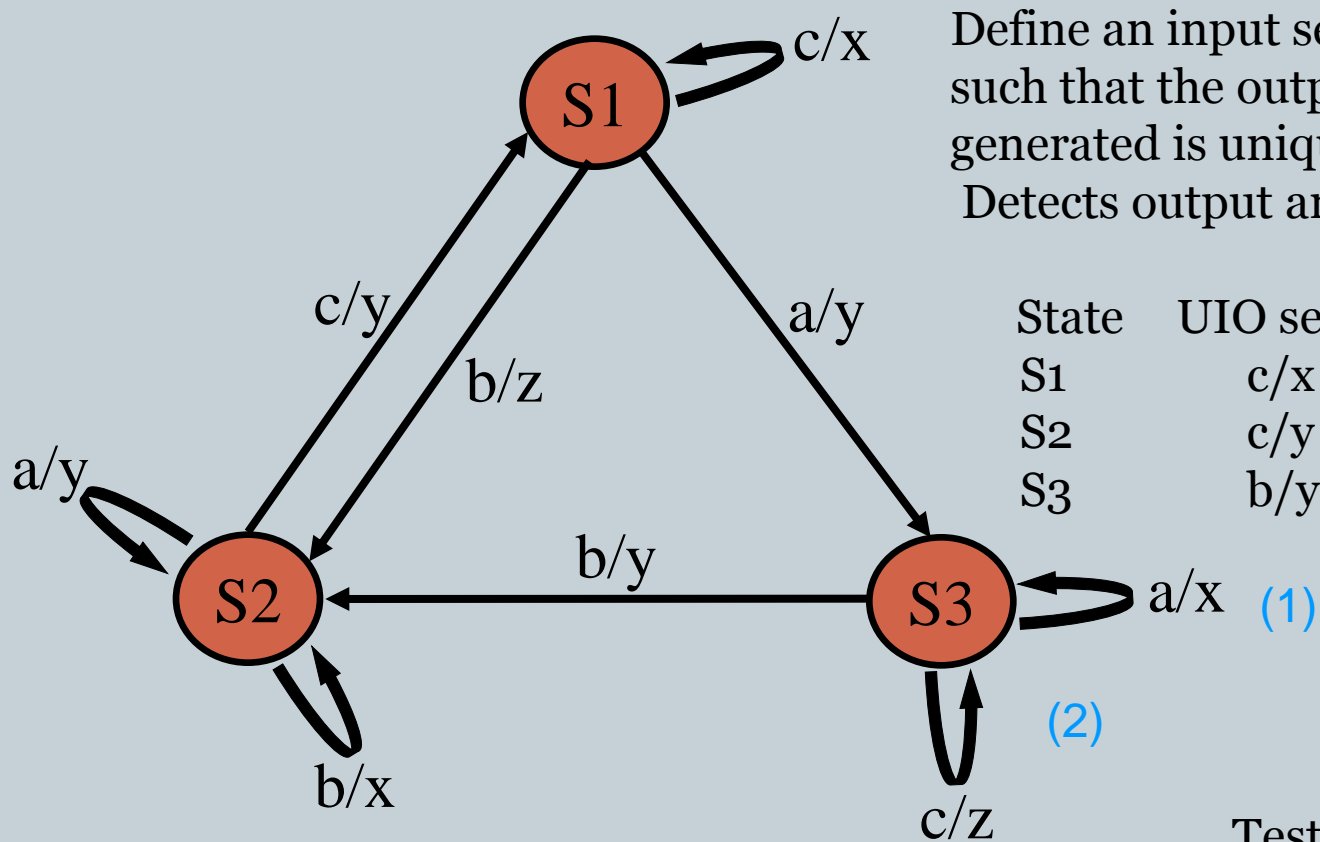
# Solutions to observability issue

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- To solve this problem different methods have been proposed:
  - DS (Distinguishing Sequence)
  - UIO (Unique Input/Output Sequence)
  - W (Distinction Set)

# Unique Input/Output sequence (UIO sequence)

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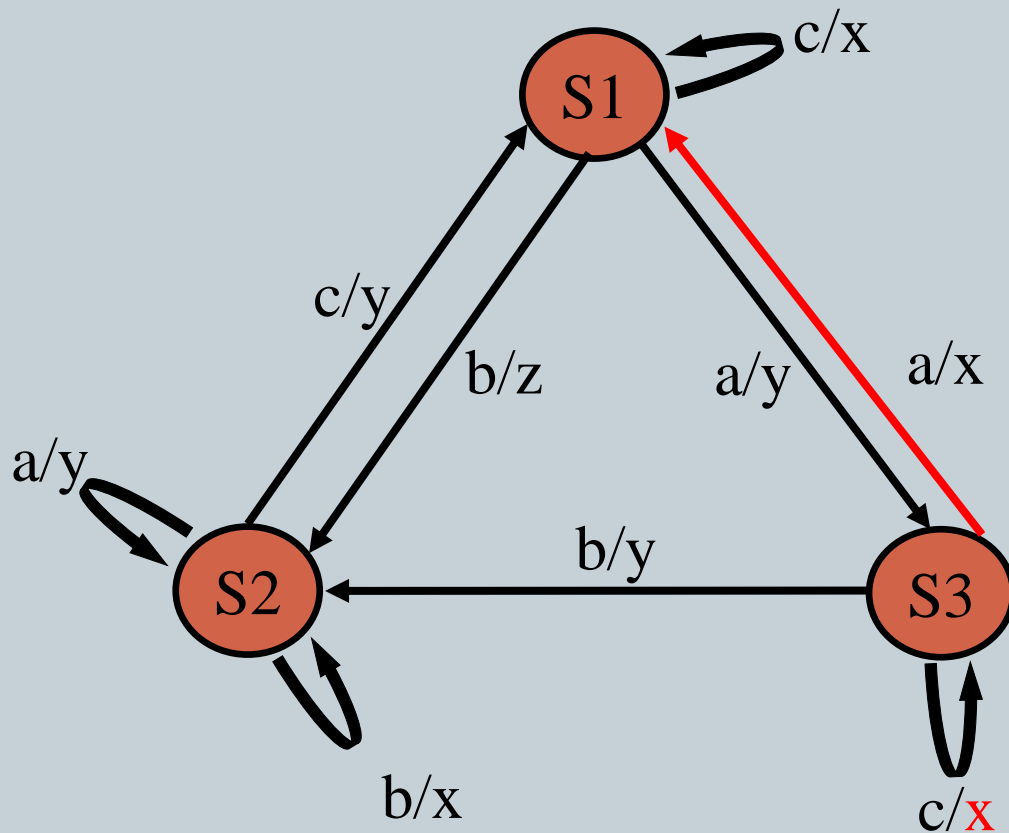
Define an input sequence for each state such that the output sequence generated is unique to that state.  
Detects output and transfer faults.

State	UIO sequences
S1	c/x
S2	c/y
S3	b/y

Test of (1): a/y a/x b/y  
Test of (2): a/y c/z b/y

# Transfer and output error detection

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Test of (1):  $a/y$   $a/x$   $b/y$

Test of (2):  $a/y$   $c/z$   $b/y$

Application du test of (1) to the implementation:  $a/y$   $a/x$   $b/z$  (transfer error)

Application of test (2) to the implementation:  $a/y$   $c/x$  (output error)

Faulty Implementation



# Limitations of active testing

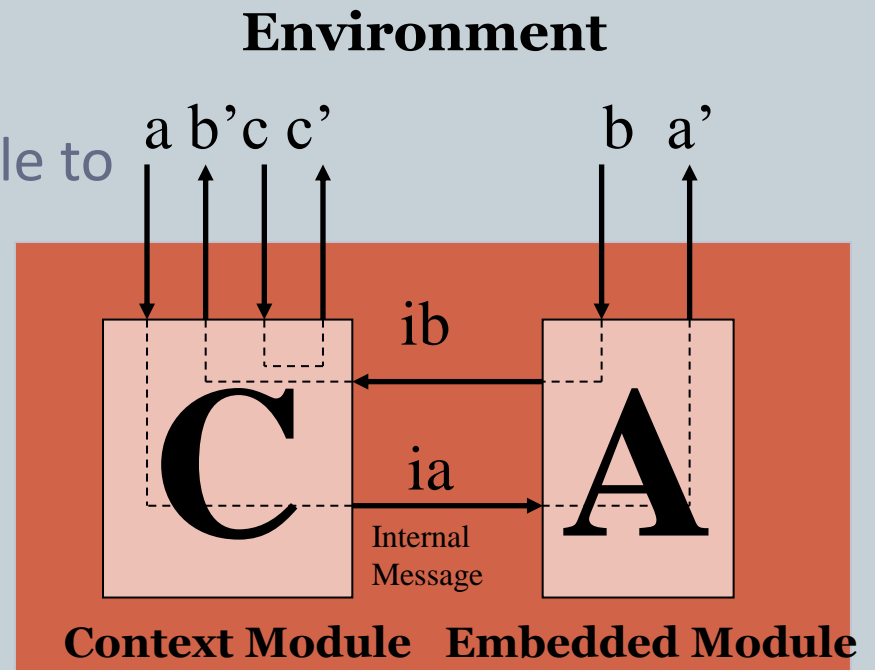
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- Non applicable when no direct access to the implementation under test
- Semi- controllable interfaces (component testing)
- Interferences on the behaviour of the implementation

# Components Testing

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- Test in context, embedded testing:
  - Tests focused on some components of the system, to avoid redundant tests
  - Interfaces semi-controllables
  - In some cases it is not possible to apply active testing



# Why passive testing?

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- **Conformance testing is essentially focused on verifying the conformity of a given implementation to its specification**
  - It is based on the ability of a tester that stimulates the implementation under test and checks the correction of the answers provided by the implementation
- **Closely related to the controllability of the IUT**
  - In some cases this activity becomes difficult, in particular:
    - ✦ if the tester has not a direct interface with the implementation
    - ✦ or when the implementation is built from components that have to run in their environment and cannot be shutdown or interrupted (for long time) in order to test them

# Controllability and observability issues in passive testing

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- **Controllability**

- No **controllability** issue because no interaction with the implementation under test

- **Observability**

- It is assumed that to perform passive testing it is necessary to observe the messages exchanges between modules.
- Passive testing is a Grey Box testing technique

- **Fault detection using passive testing**

- It is possible to detect output faults
- It is possible to detect transfer faults under some hypothesis: to initialise the IUT in order to be sure that the implementation is in the initial state and then perform passive testing

# Invariant based passive testing approach

- In this approach a set of properties are extracted from the specification or proposed by the protocol experts, and then the trace resulting from the implementation is analyzed to determine whether it validates this set of properties.
- These extracted set of properties are called invariants because they have to hold true at every moment.

# Invariant based passive testing approach

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- Definition: an invariant is a property that is always true.
- Two test steps:
  - Extraction of invariants from the specification or proposed by protocol experts
  - Application of invariants on execution event traces from implementation
- Solution: I/O invariants

# Test by invariants: I/O invariants

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- An invariant is composed of two parts :
  - The test (an input or an output)
  - The preamble (I/O sequence)
- 3 kind of invariants :
  - Output invariant (simple invariant)
  - Input invariant (obligation invariant)
  - Succession invariant (loop invariant)

# Test by invariants : Simple (Output) invariant

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- Definition : invariant in which the test is an output
- Meaning : « immediatly after the sequence *préambule* there is always the expected output »
- Example :

$(i_1 / o_1) (i_2 / o_2)$

(preamble in blue, expected output in red)



# Test by invariants : Obligation (Input) invariant

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- Definition : invariant in which the test is an input
- Meaning : « immediatly before the sequence *preamble* there is always the input *test* »
- Example :

$(i_1 / o_1) (i_2 / o_2)$

(preamble in blue, test in red)

# Test by invariants : succession invariant

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- Definition : I/O invariant for complex properties (loops ...)
  - Example :
    - the 3 invariants below build the property :  
« only the third  $i_2$  is followed by  $o_3$  »
- $(i_1 / o_1) (i_2 / o_2)$
- $(i_1 / o_1) (i_2 / o_2) (i_2 / o_2)$
- $(i_1 / o_1) (i_2 / o_2) (i_2 / o_2) (i_2 / o_3)$

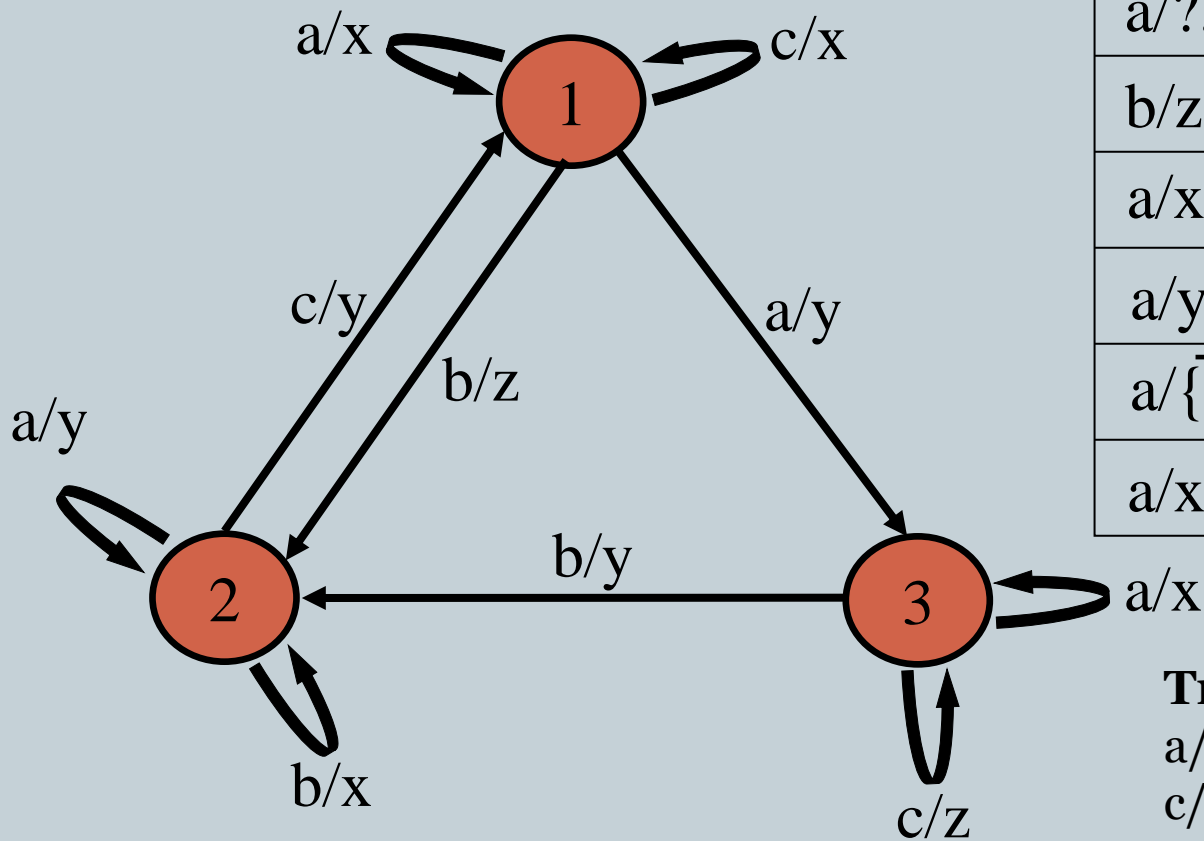
# Simple invariant

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- A trace as  $i_1/O_1, \dots, i_{n-1}/O_{n-1}, i_n/\mathbf{O}$  is a simple invariant if each time that the trace  $i_1/O_1, \dots, i_{n-1}/O_{n-1}$  is observed, if we obtain the input  $i_n$  then we necessarily get an output belonging to  $\mathbf{O}$ , where  $\mathbf{O}$  is included in the set of expected outputs.
- $i/o, *, i'/\mathbf{O}$  means that if we detect the transition  $i/o$  then the first occurrence of the symbol  $i'$  is followed by an output belonging to the set  $\mathbf{O}$ .
- $*$  replaces any sequence of symbols not containing the input symbol  $i'$  and  $?$  replaces any input or output.

# Example

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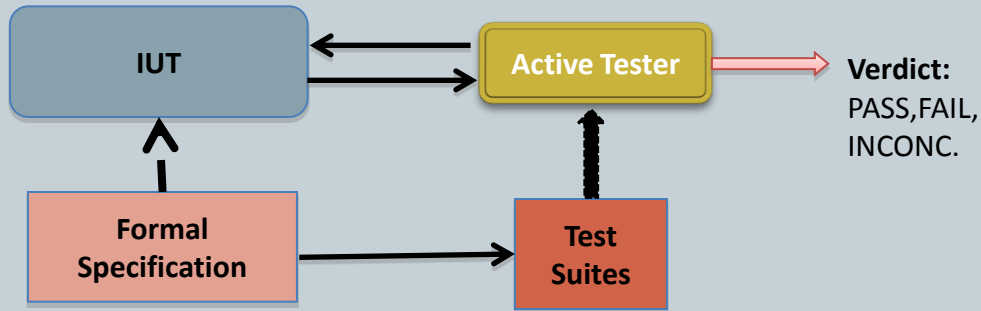
Invariants	Verdict
$a/? , c/z , b/\{y\}$	<i>True</i>
$b/z , a/\{x\}$	<i>False</i>
$a/x , * , b/\{y, z\}$	<i>True</i>
$a/y , ?/\{\bar{z}\}$	<i>False</i>
$a/\{\bar{x}\}$	<i>False</i>
$a/x , * , ?/\{\bar{y}\}$	<i>True</i>

## Traces

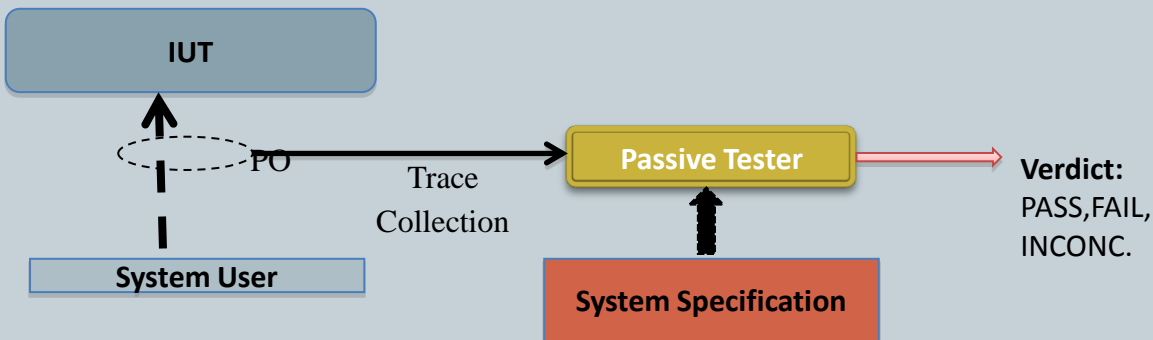
$a/y \ c/z \ b/y \ a/y \ a/x \ c/z \ b/y$   
 $c/x \ a/y \ a/x \ c/z \ b/y$   
 $c/y \ a/x \ b/z \ b/x \ a/y$

# Passive vs Active Testing

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- 😊 Possibility to focus on a specific part of the specification
- 😊 Full test generation automation
- 😞 Needs a model
- 😞 May modify (crash) the IUT behavior



- 😊 No interferences with the IUT
- 😊 No models needed
- 😊 Full monitoring automation
- 😞 Grey box testing

# Related Work

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- Monitoring of routing protocols for ad hoc (OLSR protocol) and mesh networks based on a distributed approach (Batman protocol) (TSP)
- Monitoring for secure interoperability – Application to a multi-source information system (TSP)
- Monitoring with time constraints (C. Andrés, M. Nuñez and M. Merayo)
- Other works by (T. Jeron and H. Marchand, A. Ulrich and A. Petrenko)

# Run Time Verification

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- Approach proposed by researchers of verification (model checking) community
- Passive testing developed by the testing community
- EAGLE and RuleR tools proposed by Barringer and al. in 2004 and 2010 respectively, based on temporal logics and rewriting rules for properties description
- Others tools: Tracematches, [Avgustinov et al. 2007], J-LO [Bodden 2005] and LSC [Maoz and Harel 2006]

# Active and Passive Testing in the INTER-TRUST project

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## Interoperable Trust Assurance Infrastructure



**indra**



UNIVERSIDAD DE MURCIA



- Project co-funded by the European Union under the Information and Communication Technologies theme of the 7th Framework Programme for R&D ICT-2011.1.4 Trustworthy ICT contract n. 317731
- November 2012 – April 2015 (30 months)
- [www.inter-trust.eu](http://www.inter-trust.eu)



# INTER-TRUST project Overall Objectives

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- **Develop a dynamic and scalable framework**
  - to support **trustworthy** services in **heterogeneous** networks and devices
  - based on the enforcement of **interoperable and changing** security policies
- **Addressing the needs of developers, integrators and operators**
  - to **develop and operate** systems in a secure trusted manner
  - dictated by **negotiated** security policies through dynamic security SLAs
- **Separate the security concerns from the functional requirements => AOP**

# INTER-TRUST project Overall Objectives

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## Validation

Validate the architecture, techniques and tools developed using **two completely different case studies** with complex, high-demanding critical services

- V2X communications

- E-Voting

# INTER-TRUST framework

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- The **INTER-TRUST framework** allows the secure interoperation enforcement and supervision between communicating and heterogeneous systems. It allows:
  - **The negotiation and adaptability** of security policies according to the available resources and changes in the environment
  - **The dynamic deployment** of negotiated security policies using a selected AOP framework
  - **The automatic verification and validation** of security policies by means of testing & monitoring techniques

# I-T Framework architecture

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*DEVICE*

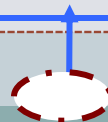
Dynamic  
specification of  
security policies  
(SLAs)

Dynamic  
deployment of  
security policies

Application  
+ Context  
Monitoring

Testing for  
Vulnerabilities  
detection

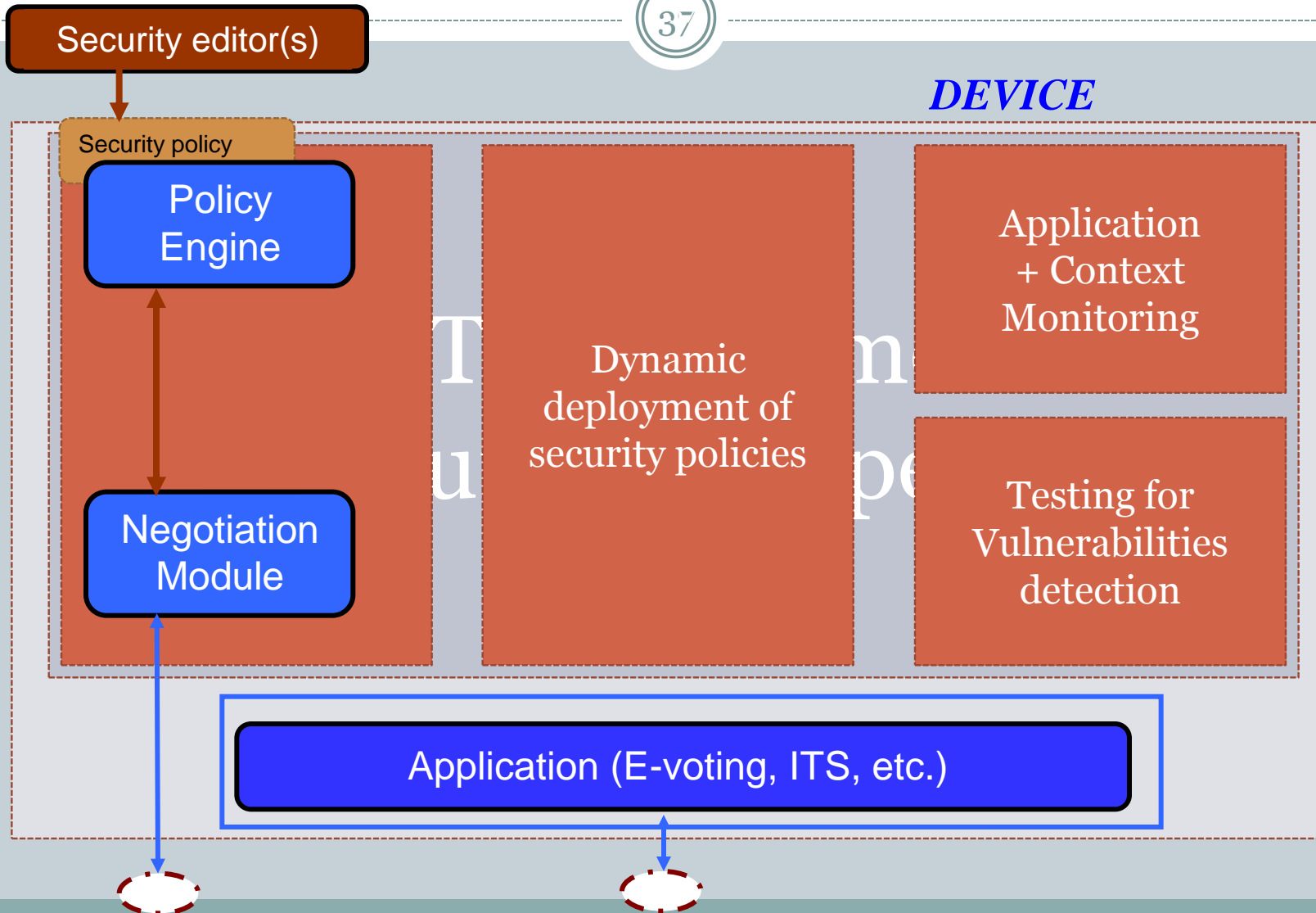
Application (E-voting, ITS, etc.)



# I-T Framework architecture

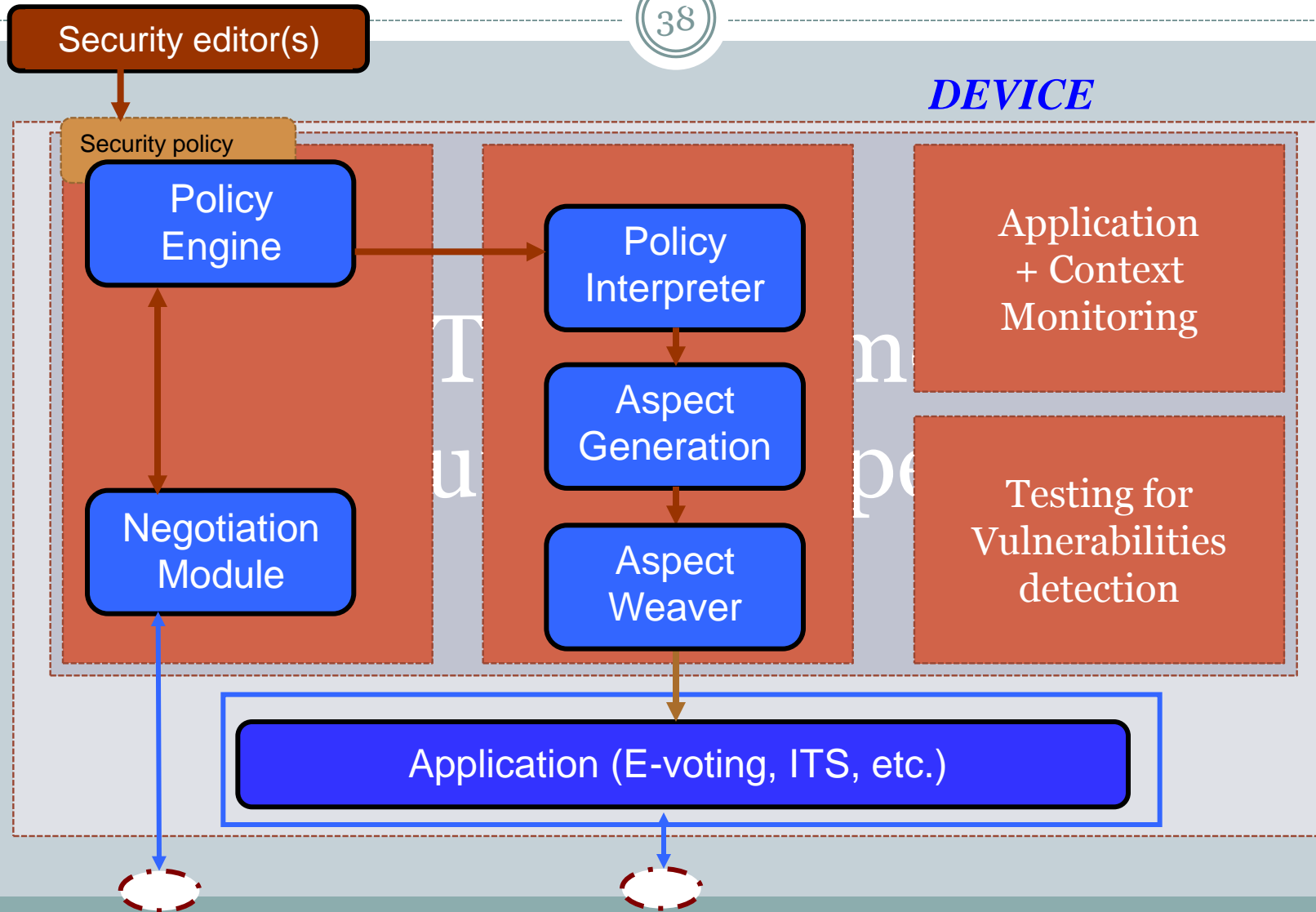
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*DEVICE*



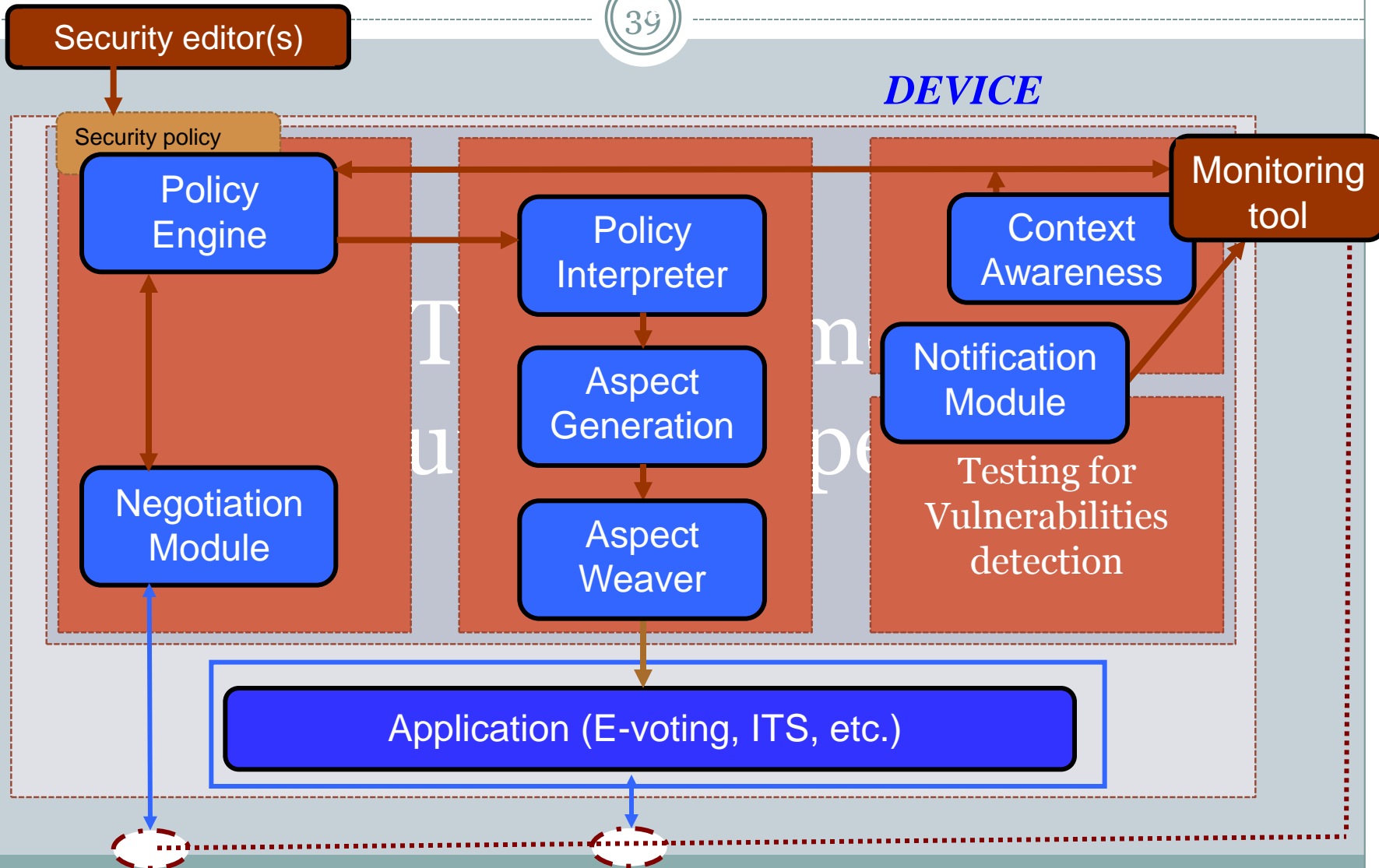
# I-T Framework architecture

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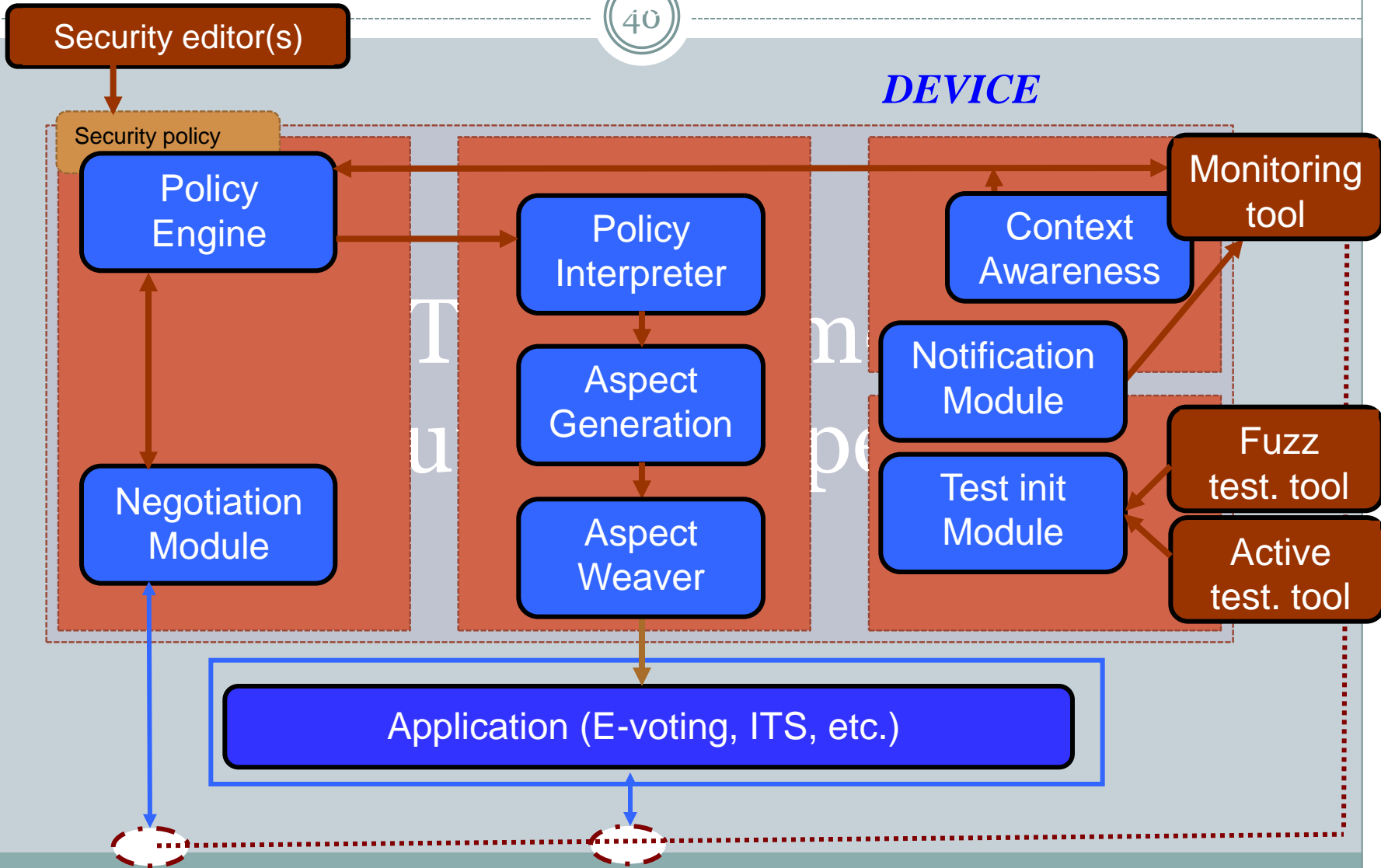


# I-T Framework architecture

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# I-T Framework architecture



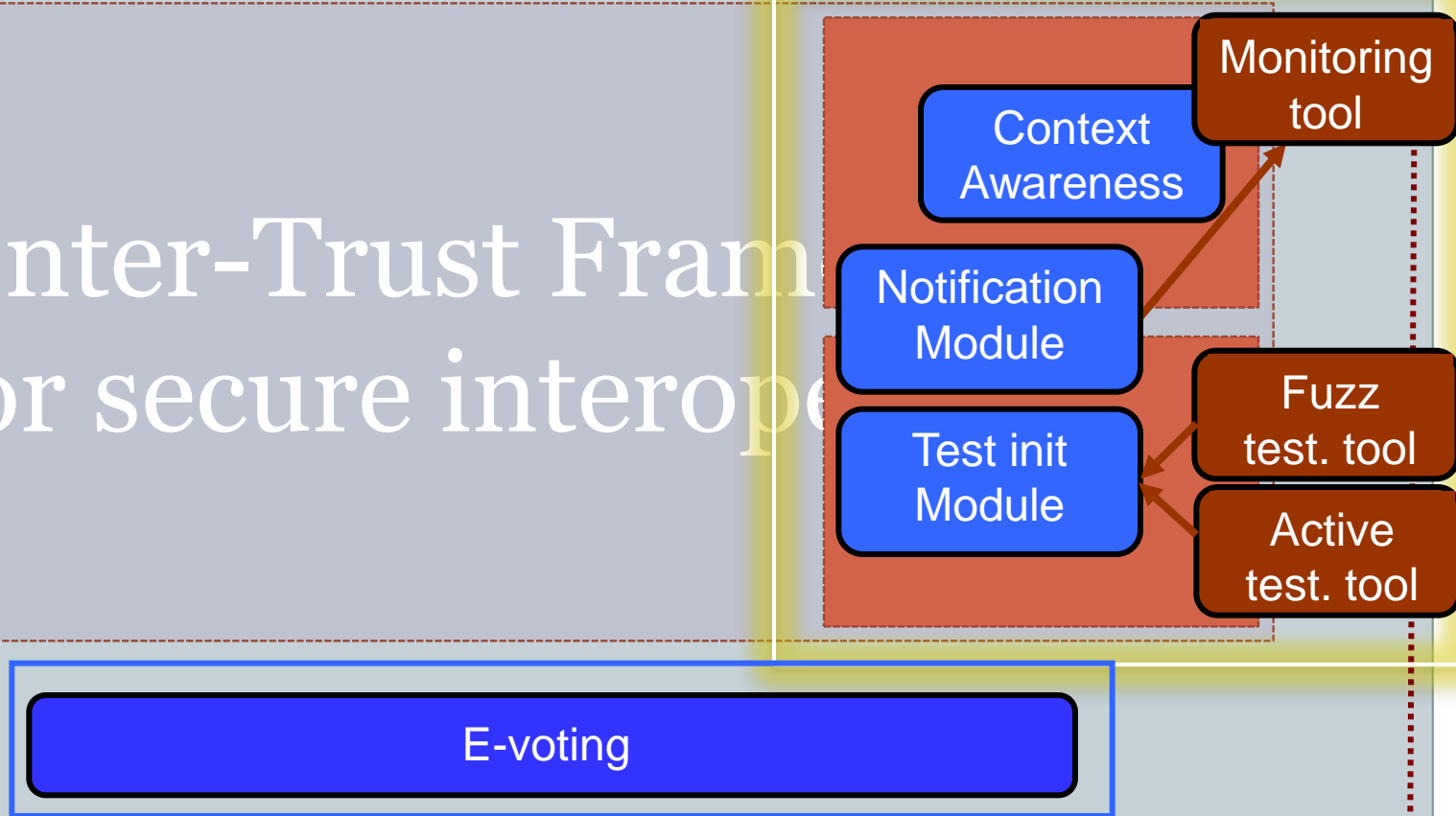


# I-T Framework architecture

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Inter-Trust Framework  
for secure interoperation

*DEVICE*



# Motivation

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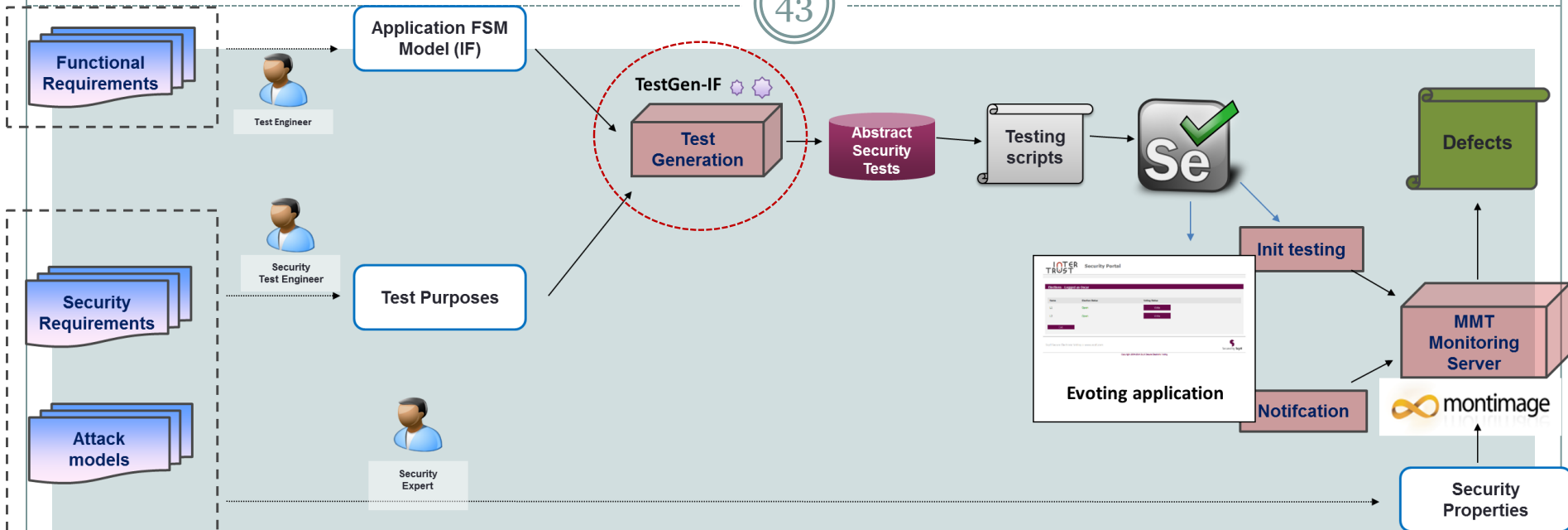


- Why testing ? (testing phase)
  - Vulnerabilities can be introduced by AOP used in Inter-trust
    - ✦ Functional testing
    - ✦ Check the respect of weaved security policies (aspects)
    - ✦ Check the robustness of the target application
    - ✦ Detect vulnerabilities
    - ✦ Simulate attacks
- Why monitoring ? (testing & operation phases)
  - Same as above
  - + detecting context changes (context awareness) at runtime



# Model based testing: TestGen-IF

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- Generation of tests from IF model and test purposes
  - Target: functional, security properties, attacks\*
- Execution relying on Selenium (Web interface)
- Detecting failures using MMT

# IF FSM Model

Elections - Logged as Bob

Fi

Vote Verification  Scytl Online Voting

L1: Interest market

user  
pose  
ons

Question L1.1 Log Out Signed in as login1

Q1: Which of the

Voting Receipt

Step 3 of 3

A1: Sports Your vote has been issued correctly. The voting receipt is your receipt document. We recommend that you print it.

Question L1.2 Receipt for the Voter

Q2: Do you agree with the proposal of having acces to a virtual newspaper in working hours?

The E-voting application has been specified as an extended finite state machine (IF language)

e1zEame/NmPI+uIkB azEHdrXas8CZyPXLvhVH/UhDog8Qznh 6UNyZYqNfaqBUCYvmpvem5sCQEHoJ8f kt94w9mkMatChben9e3FEVCYnzDD+mK f5mf8L21Yr4i6VVk/h+iinv  
Dk5BGA1rex8NJqLu5CYfv8JmN v4dWbFuYCDK2xkSRfAxv5v16J01jBge +w56Qe9dI61e19Rdk2eKv99KurHgKv8 tW

This state presents the

In this step the vote choices are displayed. The user has to fill the vote form. The step is the effective vote

Q2: Do you agree with the proposal of having acces to a virtual newspaper in working hours?

Yes

No

Exit

Default Message

# Test Objective specification

Test generation with TestGen-IF

Test Objective

Choosing Privacy Options

Get details

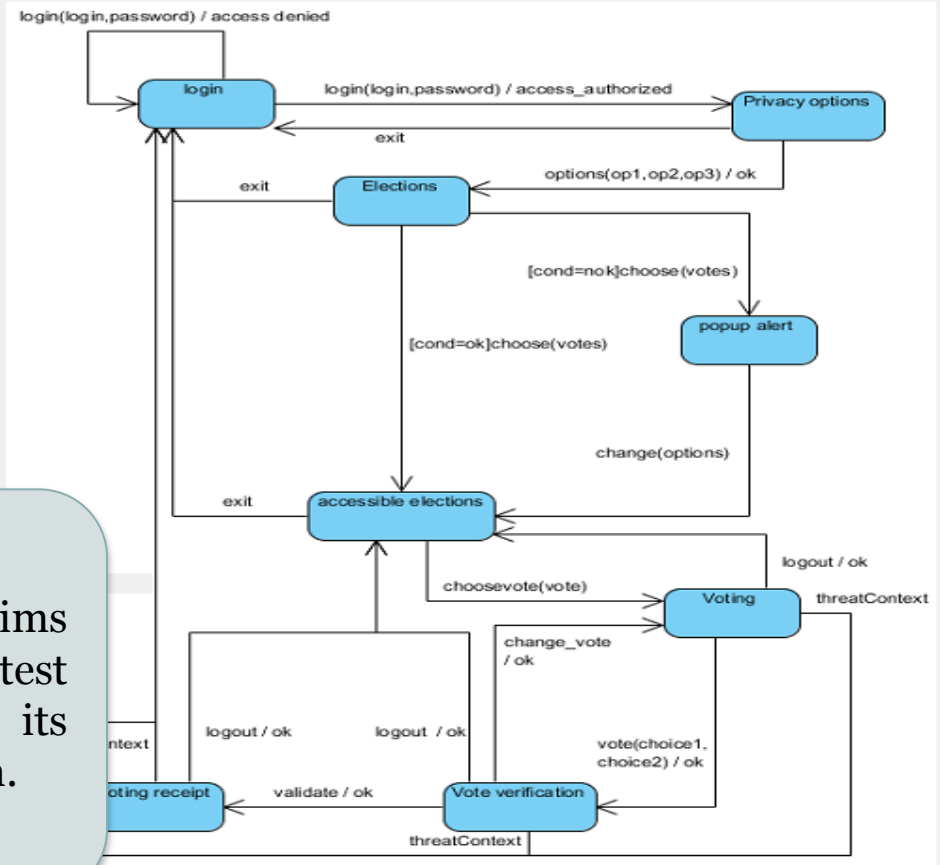
Description

The tester chooses specific sets of privacy options. Our objective is to check the behavior of the system when the user chooses a certain combination of privacy options.

Formal specification

```
obji = cond1^cond2^cond3^cond4^cond5 for i = 1...27  
cond1 = process: instance = {server}0  
cond2 = state: source: privacy_options  
cond3 = state: destination: election  
cond4 = action: input  
options(optionpop1, optionpop2, optionpop3)
```

This part of the TestGen-IF tool aims to choose the test objective. Each test objective is presented with its description and formal specification.



# Test case Generation

SWT Application

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Test generation with TestGen-IF

Test Objective

Force Brute Version 1

Description

The tester tries to find the correct password in order to connect as a Bob user.  
Our objective is to check the behavior of the system when an attacker (misuser) tries to find the password by guessing.  
The IF model contains several possible couples (login, password) that will be tested until the end of the dictionary

Formal specification

```
OBJ(1) = OBJ(ord) = {obj1, obj2, obj3, obj4}
obj1,obj3 = cond1^cond2^cond3^cond4^cond5
cond1 = process: instance = {server}0
cond2 = state: source: login
cond3 = state: destination: login
cond4 = action: input login(login1,pass2)
```

Test case

```
?login{login1,pass1} !accessAuthorized{}
?options{optionpop11,optionpop21,optionpop31} !ok{}
?choose{votes1}
?login{login1,pass2} !accessDenied{message1}
?login{login1,pass3} !accessDenied{message1}
?login{login1,pass2} !accessDenied{message1}
?login{login1,pass1} !accessAuthorized{}
```

IP Address  Port

The test generation of abstract test cases based on an algorithm called "Hit or Jump"

Abstract Security Tests

# Automatic test execution

SWT Application

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### Test generation with TestGen-IF

**Test Objective**

Force Brute Version 1 Get details

**Description**

The tester tries to find the correct password in order to connect as a Bob user.  
Our objective is to check the behavior of the system when an attacker (misuser) tries to find the password by guessing.  
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**Formal specification**

OBJ(1) = OBJ(ord) = {obj1, obj2, obj3, obj4}  
obj1,obj3 = cond1^cond2^cond3^cond4^cond5  
cond1 = process: instance = {server}0  
cond2 = state: source: login  
cond3 = state: destination: login  
cond4 = action: input login(login1,pass2)

Generate Test case

**Test case**

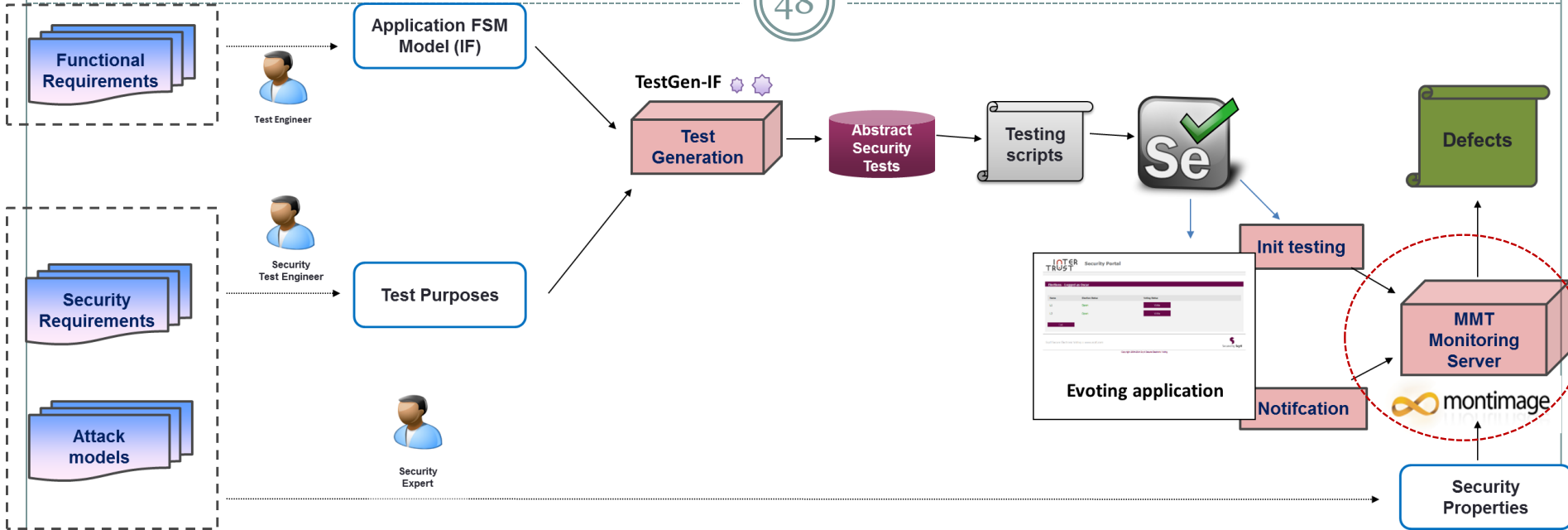
```
?login{login1,pass1}!accessAuthorized{  
?options{optionpop11,optionpop21,optionpop31}!ok{  
?choose{votes1}  
?login{login1,pass2}!accessDenied{message1}  
?login{login1,pass3}!accessDenied{message1}  
?login{login1,pass2}!accessDenied{message1}  
?login{login1,pass1}!accessAuthorized{
```

IP Address: 192.168.1.1    Port: 8081    Execute

```
graph TD
    login((login)) -- "login(login,password) / access_denied" --> login
    login -- "login(login,password) / access_authorized" --> privacy((Privacy options))
    privacy -- "exit" --> login
    privacy -- "options(op1,op2,op3) / ok" --> elections((Elections))
    elections -- "exit" --> login
    elections -- "[cond=no]choose(votes)" --> popup((popup alert))
    elections -- "[cond=ok]choose(votes)" --> popup
    popup -- "change(options)" --> elections
    elections -- "logout / ok" --> voting((Voting))
    voting -- "choosevote(vote)" --> elections
    voting -- "logout / ok" --> voting_receipt((Voting receipt))
    voting -- "vote(choice1,choice2) / ok" --> vote_verification((Vote verification))
    vote_verification -- "change_vote / ok" --> voting
    vote_verification -- "validate / ok" --> voting_receipt
    voting_receipt -- "threatContext" --> login
    vote_verification -- "threatContext" --> login
```

# Monitoring tool (MMT)

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- Detecting failures using MMT
  - Events based detection
  - Properties as FSMs or as LTL properties



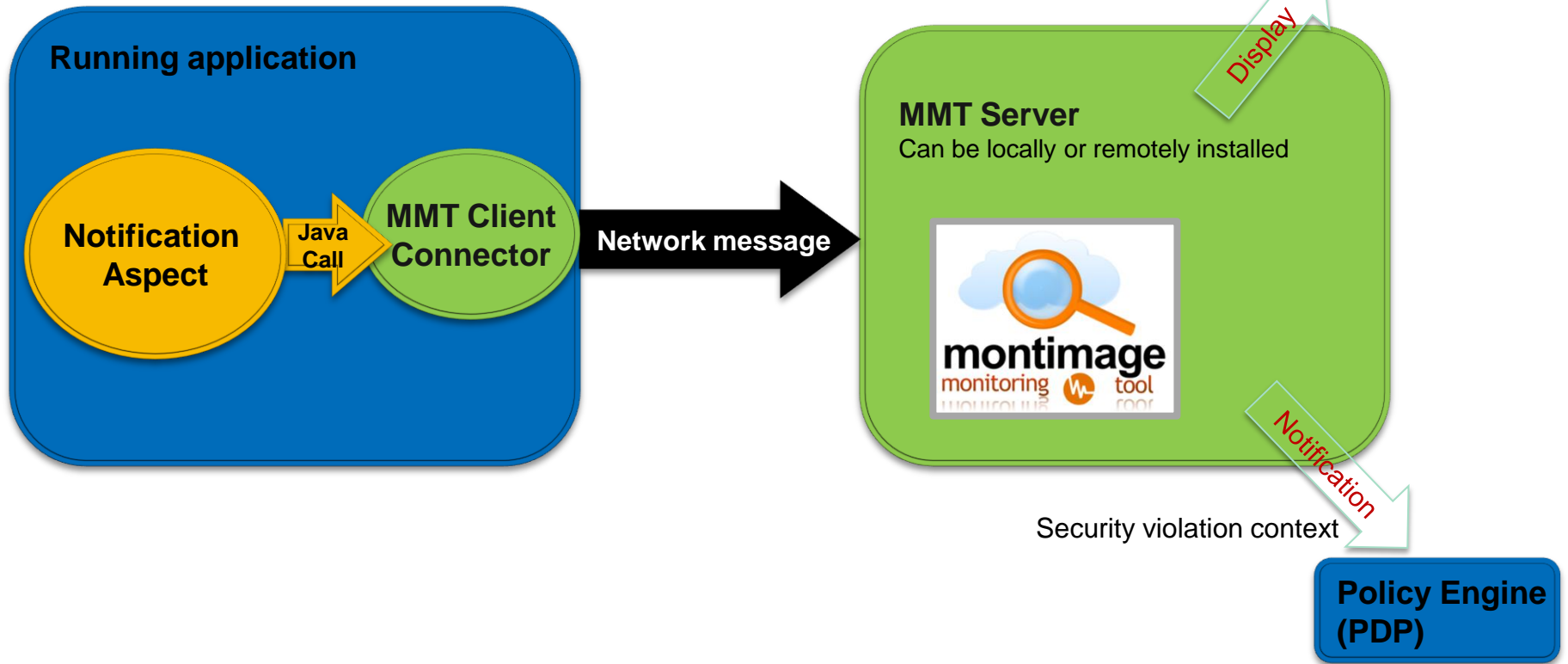
# Monitoring

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- **2 main usages:**
  - During the testing phase to complement the testing tools and provide a verdict
  - During the operation phase to monitor security and application context
  
- **Relies on data collected at different levels**
  - Network (ex. CAM messages)
  - Application internal events (notification module)\*
  - System status (CPU and memory usage)

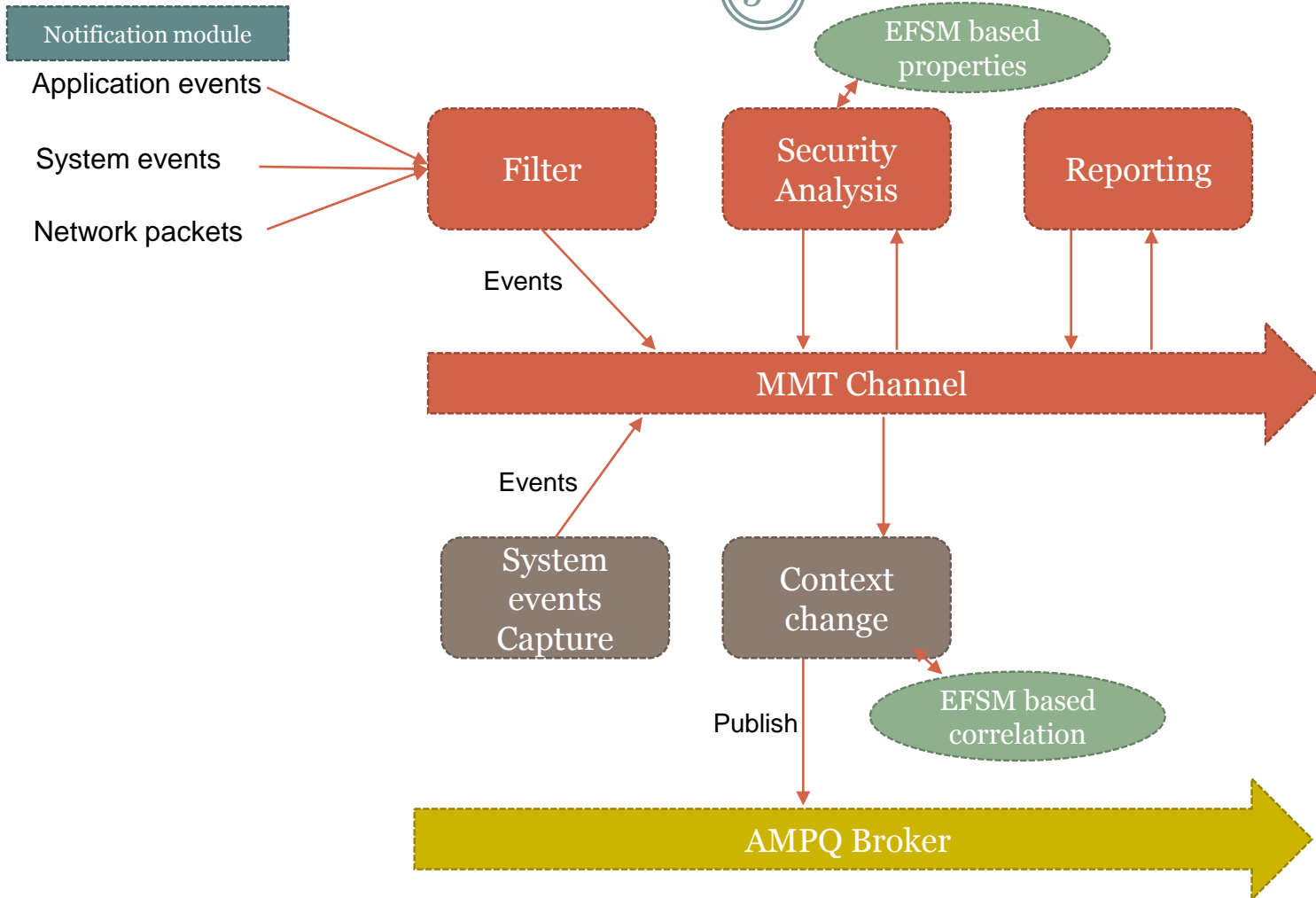
# Application internal events notification

50



# MMT internal behavior

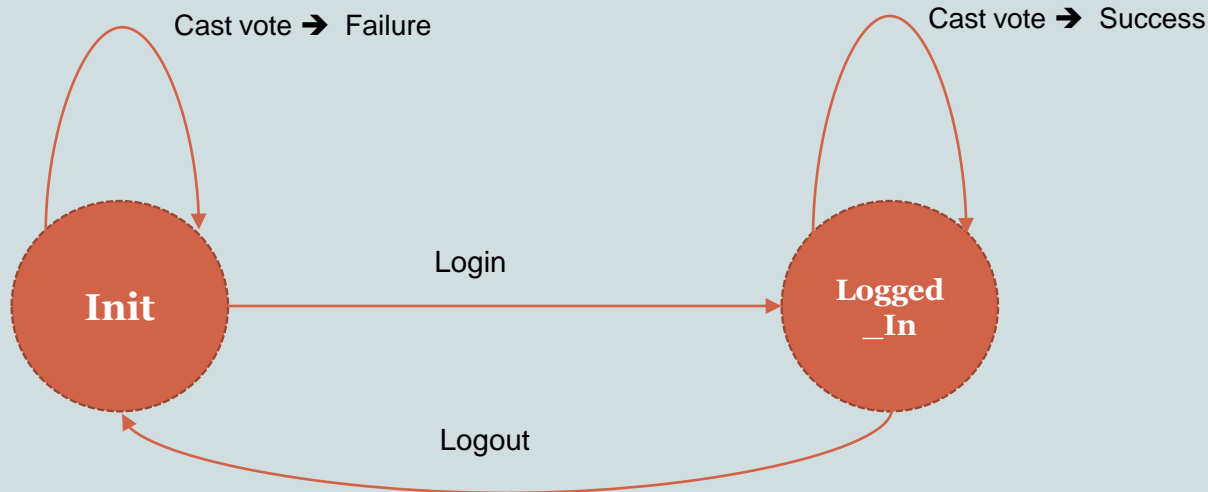
52



# Analysis and failure detection

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- Evoting test case – Advanced authentication option
  - Example of property: Only authenticated voters can cast their votes



# MMT analysis dashboard – Security property

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localhost:4567/mmt-sec

## Security Dashboard



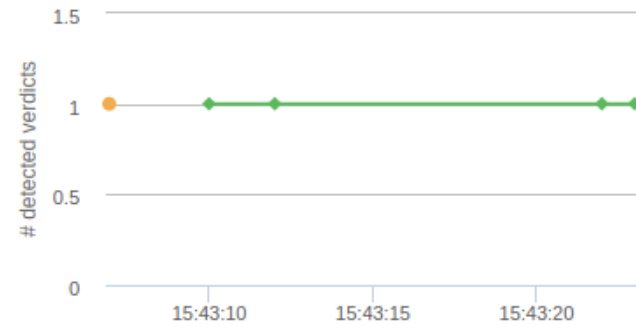
Detected failures for security property 1: Only logged voters can cast their votes

Total Failure Verdicts

1

Total Success Verdicts

4



# MMT analysis dashboard – Attack detection

55

localhost:4567/mmt-sec

## Security Dashboard



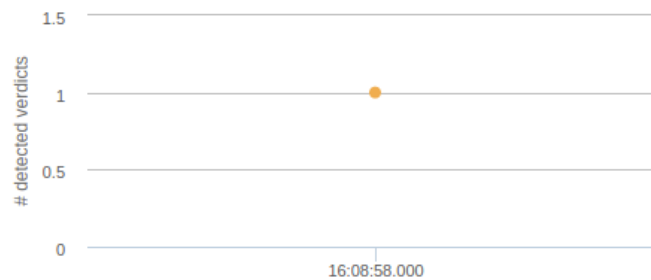
### Detected attacks for attack scenario 1: Brute force attack

Total Failure Verdicts

1


Total Success Verdicts

0



# Summary

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- Model based test generation for security purposes (TestGen-IF)
- Correlation of data from different sources (Network, application, system)
- Detection of attacks and failures at runtime  
  
reaction
- Brings dynamicity to system by adapting to different contexts

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